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*** Revision History**

Date	Rev. No	Page	Summary
Feb. 26, 2010	000	-	First Issued
May. 11, 2010	001	-	<ul style="list-style-type: none">• LED Vendor multiplication<ul style="list-style-type: none">- AS IS : S-LED- TO BE : S-LED + SSC (Seoul Semiconductor) Model Code Added : LTY[Z]460HJ02-102 (SSC)

General Description

Description

LTY[Z]460HJ02 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 46.0" is 1920 x 1080 and this model can display up to 1.07Giga colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide an excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and High Definition TV (HDTV).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, high aperture ratio, fast response time
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 89^\circ$)
- Full HD (1920 x 1080 pixels) resolution (16:9)
- Low Power consumption
- WLED (White Light Emitting Diode) Backlight
- miniLVDS (Low Voltage Differential Signaling) Interface

General Information

Items	Specification	Unit	Note
Module Size	1093(H _{TYP}) x 664(V _{TYP})	mm	$\pm 1.0\text{mm}$
	29.3(D _{MAX})		
Weight	11300(Max)	g	
Pixel Pitch	0.17675(H) * 3 x 0.53025(V)	mm	
Active Display Area	1018.08(H) x 572.67(V)	mm	
Surface Treatment	Haze 4.5%, Hard-coating (3H)		
Display Colors	8bit + 2bit FRC – 1.07G	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	400	cd/m ²	Typ.

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	GND-0.5	13	V	(1)
Storage temperature	T_{STG}	-20	65	°C	(2)
Operating Temperature	T_{OPR}	0	50	°C	(2)
Panel surface temperature	T_{SUR}	0	65	°C	(3)
Shock (non - operating)	S_{NOP}	-	50	G	(4)
Vibration (non - operating)	V_{NOP}	-	1.5	G	(5)

Note (1) $T_a = 25 \pm 2$ °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 93.8 % RH Max. ($T_a \leq 40$ °C)

b. Maximum wet-bulb temperature at 40 °C or less. ($T_a \leq 40$ °C)

c. No condensation

(3) Polarizer will not be damaged in this range, even though abnormal visual problems occur in T_{SUR} range.

(4) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis

(5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

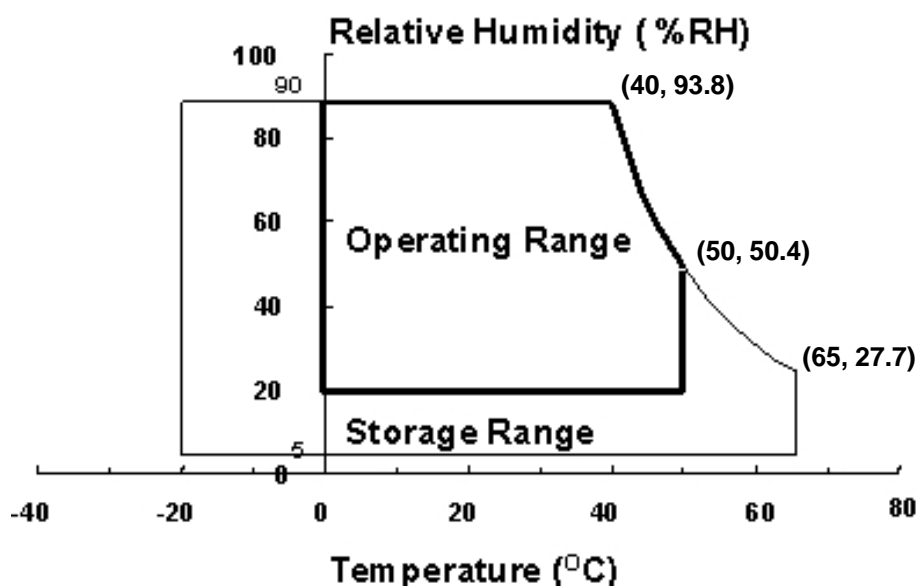


Fig. Temperature and Relative humidity range

2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON BM-7,SPECTRORADIOMETER SR-3

(Ta = 25 ± 2°C, VDD=12V, fv= 60Hz, f_{DCLK}=148.5MHz, Dim = 100%)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal $\theta_{\text{L,R}}=0$ $\theta_{\text{U,D}}=0$ Viewing Angle	*2500	5000	-		(3) SR-3
Response Time	Rising	Tr		-	8	13	msec	(5) BM-7
	Falling	Tf		-	6	8		
	G-to-G [AVE]	Tg		-	6.5	-		
Luminance of White (Center of screen)		Y _L		320	400	-	cd/m ²	(6) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	0.618	TYP. +0.03		(7),(8) SR-3
		Ry			0.333			
	Green	Gx			0.315			
		Gy			0.628			
	Blue	Bx			0.155			
		By			0.050			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-	-	72	-	%	(7) SR-3	
Color Temperature		-	-	10000	-	K	(7) SR-3	
Gamma		γ	-	2.2	-			
Viewing Angle	Hor.	θ _L	C/R≥10	79	89	-	Degree	(8) SR-3
		θ _R		79	89	-		
	Ver.	θ _U		79	89	-		
		θ _D		79	89	-		
Brightness Uniformity (9 Points)		B _{uni}		-	-	30	%	(4) SR-3

* marked Items Value does not Specification above when "White stain" occurs at Center Point [Point ⑤ of Note 2.]

Note (1) Test Equipment Setup

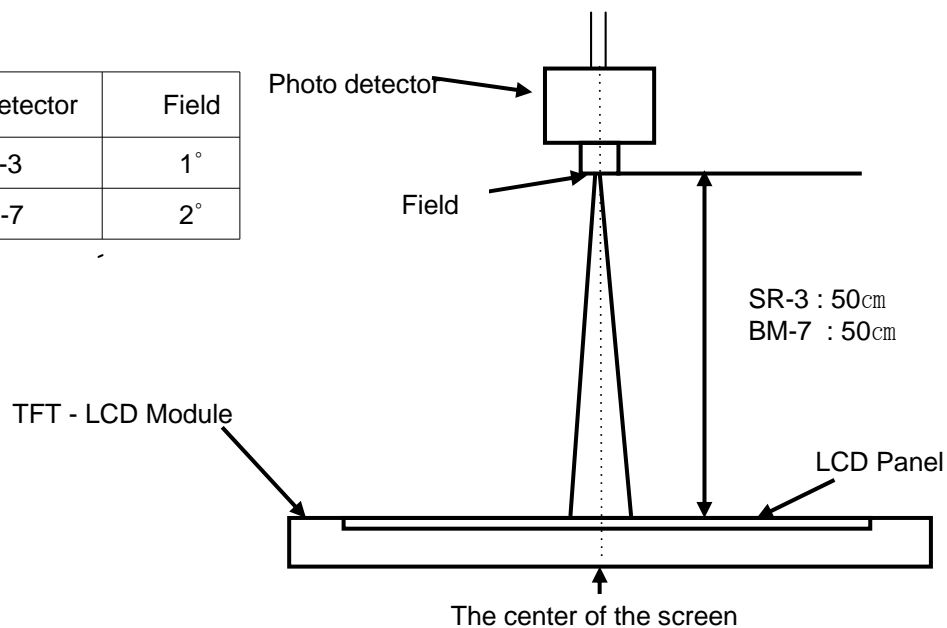
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Single lamp current @ Vdim = 100%

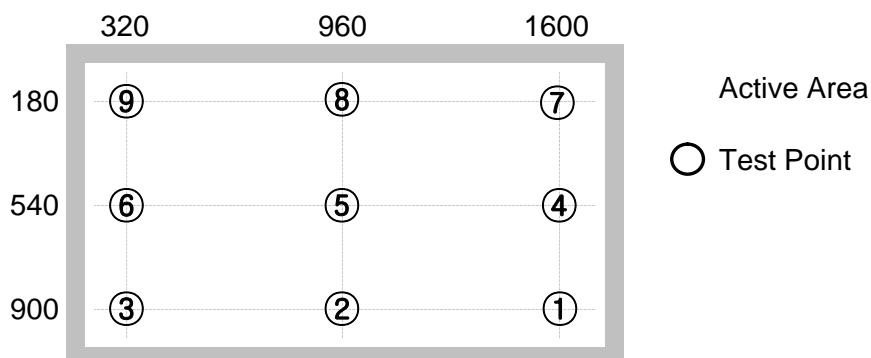
Environment condition : Ta = 25 ± 2 °C

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Photo detector	Field
SR-3	1°
BM-7	2°



Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance with all pixels white

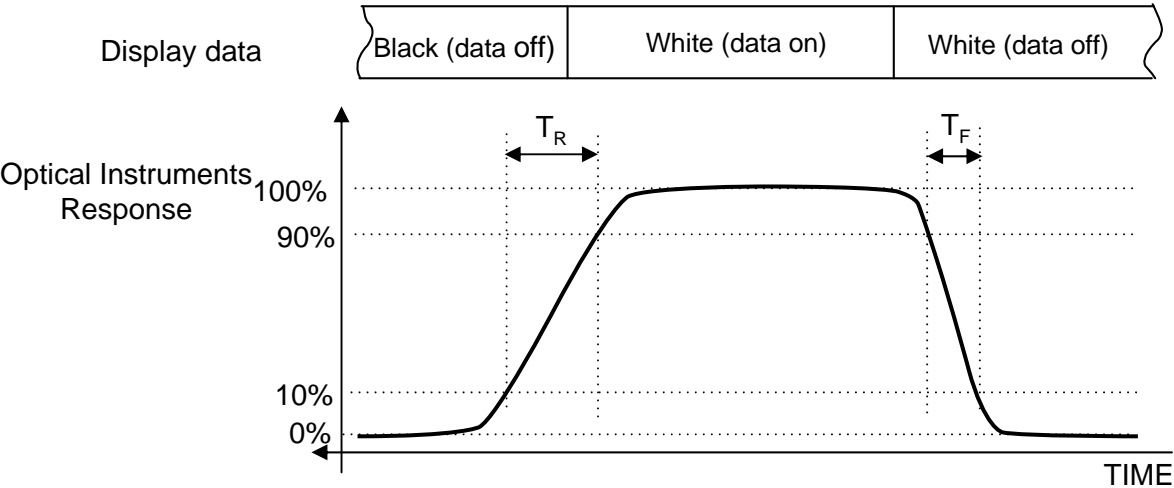
Gmin : Luminance with all pixels black

Note (4) Definition of 9 points brightness uniformity

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

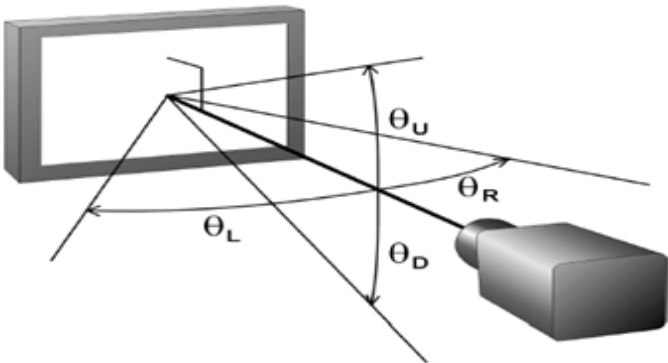
Note (5) Definition of Response time : Sum of Tr, Tf



Note (6) Definition of Luminance of White : Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle
: Viewing angle range ($C/R \geq 10$)



3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

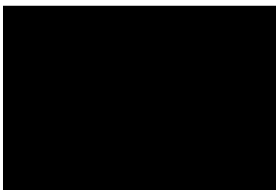
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	V_{DD}	11	12	13	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	1000	mA	(2),(3)
	(b) White		-	1000		
	(c) Mosaic		-	1000		
	(4) Max Pattern		-	1600		
Vsync Frequency	f_V	-	60	-	Hz	
Hsync Frequency	f_H	-	67.5	-	kHz	
Main Frequency	f_{DCLK}	-	148.5	-	MHz	
Rush Current	I_{RUSH}	-	-	5	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

(2) $f_V=60\text{Hz}$, $f_{DCLK} = 148.5\text{MHz}$, $V_{DD} = 12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

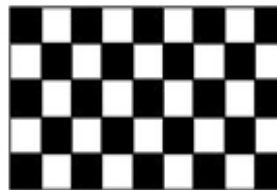
a) Black Pattern



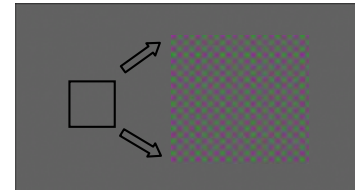
b) White Pattern



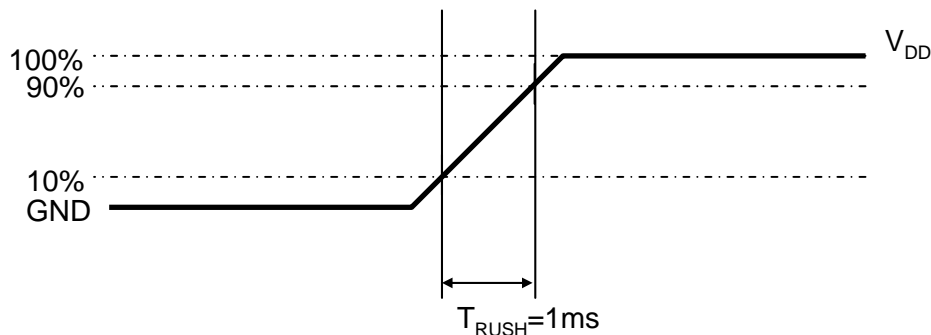
c) Mosaic



d)Max. Pattern



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH} is 1ms

3.2 Back Light Unit

The back light contains 216 LEDs.

The characteristics of lamps are shown in the following tables.

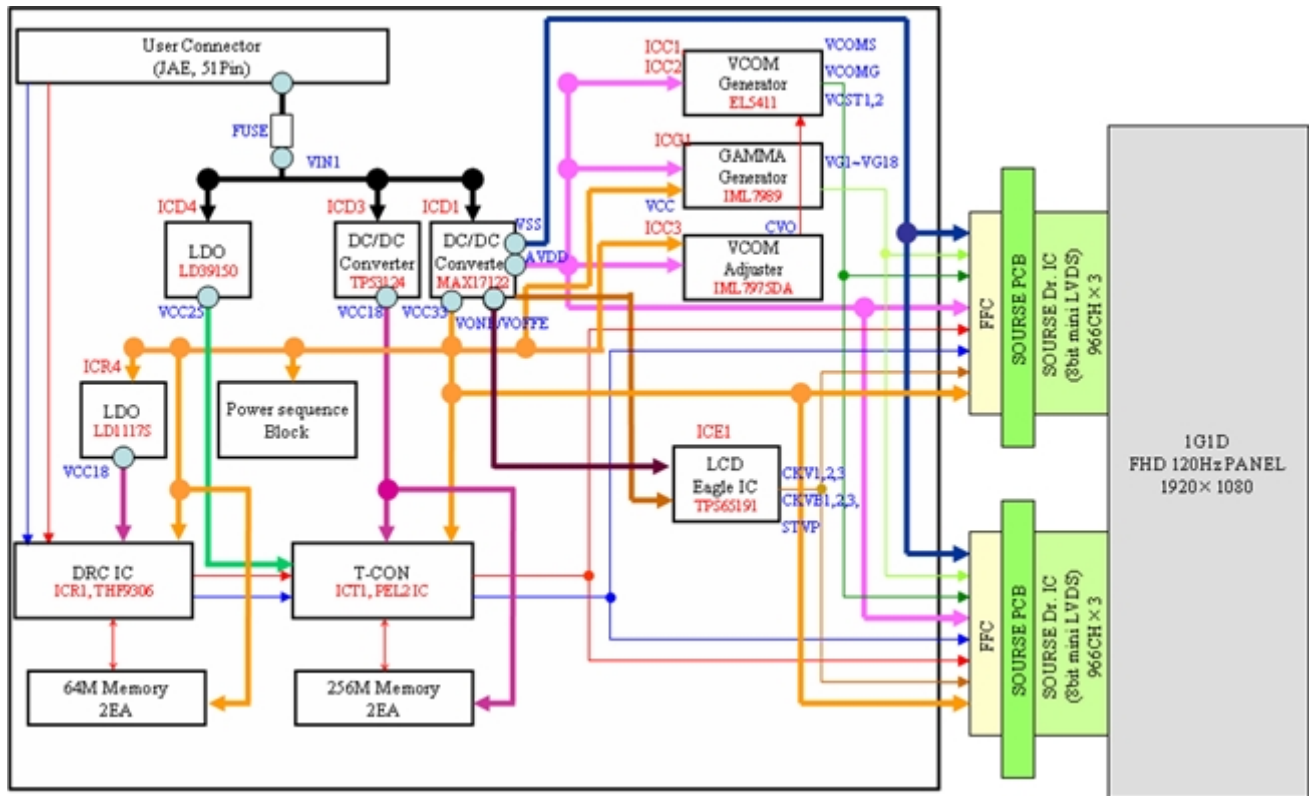
$T_a = 25 \pm 2^\circ\text{C}$

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time		Hr	30,000	-	-	Hour	(1)
Operating Current	Continuous	I_{op}	-	-	160	mA	/String
	Impulsive (120Hz/Duty50%)	I_{op}	-	-	185	mA	@110mA /Duty100%
Operating Voltage	Continuous	V_{op}	-	-	190	V	54LEDs /110mA@Tj 25°C
	Impulsive (120Hz/Duty50%)	V_{op}	-	-	205	V	Tj 25°C

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Definition of Operating Voltage : At each Strings, $I_{op} = 110.0 \text{ mArms (typ.)}$]

4. Block Diagram



5. Input Terminal Pin Assignment

5.1. Input Signal & Power (Stand Alone Mode)

5.1.1 Pin Connector : JAE(FI-RE51S)

PIN No.	Signal	Description	PIN No.	Signal	Description
1	Power	V _{DD}	26	RE[0]P	Even LVDS Signal +
2	Power	V _{DD}	27	RE[1]N	Even LVDS Signal -
3	Power	V _{DD}	28	RE[1]P	Even LVDS Signal +
4	Power	V _{DD}	29	RE[2]N	Even LVDS Signal -
5	Power	V _{DD}	30	RE[2]P	Even LVDS Signal +
6	N.C.	N.C.	31	GND	GND
7	GND	GND	32	RECLK-	Even LVDS Clock-
8	GND	GND	33	RECLK+	Even LVDS Clock+
9	GND	GND	34	GND	GND
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	RE[4]N	Even LVDS Signal -
13	RO[1]P	Odd LVDS Signal +	38	RE[4]P	Even LVDS Signal +
14	RO[2]N	Odd LVDS Signal -	39	GND	GND
15	RO[2]P	Odd LVDS Signal +	40	SCL	I2C SCL
16	GND	GND	41	SDA	I2C SDA
17	ROCLK-	Odd LVDS CLK -	42	VSYNC	Balance board Sync Signal
18	ROCLK+	Odd LVDS CLK +	43	B_INT	Bus Switching
19	GND	GND	44	N.C.	N.C.
20	RO[3]N	Odd LVDS Signal -	45	N.C.	N.C.
21	RO[3]P	Odd LVDS Signal +	46	SA_MODE	SA Mode Sel Signal
22	RO[4]N	Odd LVDS Signal -	47	N.C.	N.C.
23	RO[4]P	Odd LVDS Signal +	48	N.C.	N.C.
24	GND	GND	49	N.C.	N.C.
25	RE[0]N	Even LVDS Signal -	50	TCON_RDY	T-Con Ready
			51	SEL1	SEC Internal Use Only

5.1. Input Signal & Power (SET Mode)

5.1.1 Pin Connector : JAE(FI-RE51S)

PIN No.	Signal	Description	PIN No.	Signal	Description
1	N.C.	N.C.	26	RE[0]P	Even LVDS Signal +
2	N.C.	N.C.	27	RE[1]N	Even LVDS Signal -
3	N.C.	N.C.	28	RE[1]P	Even LVDS Signal +
4	N.C.	N.C.	29	RE[2]N	Even LVDS Signal -
5	N.C.	N.C.	30	RE[2]P	Even LVDS Signal +
6	N.C.	N.C.	31	GND	GND
7	GND	GND	32	RECLK-	Even LVDS Clock-
8	GND	GND	33	RECLK+	Even LVDS Clock+
9	GND	GND	34	GND	GND
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	RE[4]N	Even LVDS Signal -
13	RO[1]P	Odd LVDS Signal +	38	RE[4]P	Even LVDS Signal +
14	RO[2]N	Odd LVDS Signal -	39	GND	GND
15	RO[2]P	Odd LVDS Signal +	40	SCL	I2C SCL
16	GND	GND	41	SDA	I2C SDA
17	ROCLK-	Odd LVDS CLK -	42	VSYNC	Balance board Sync Signal
18	ROCLK+	Odd LVDS CLK +	43	B_INT	Bus Switching
19	GND	GND	44	N.C.	N.C.
20	RO[3]N	Odd LVDS Signal -	45	N.C.	N.C.
21	RO[3]P	Odd LVDS Signal +	46	SA_MODE	SA Mode Sel Signal
22	RO[4]N	Odd LVDS Signal -	47	N.C.	N.C.
23	RO[4]P	Odd LVDS Signal +	48	N.C.	N.C.
24	GND	GND	49	N.C.	N.C.
25	RE[0]N	Even LVDS Signal -	50	TCON_RDY	T-Con Ready
			51	SEL1	SEC Internal Use Only

5.1.2 JST 4PIN CONNECTOR (SM04B-PASS)

PIN No.	Signal	Description
1	Power	V _{DD}
2	Power	V _{DD}
3	GND	GND
4	GND	GND

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■ Option Pin Description

- B_INT : I2C BUS Switching Pull down 100Ω/10kΩ

L/OPEN	SDA/SCL LINE BECOME HI-Z
H	USER CAN ACCESS EEPROM

- SA_MODE : Direct 1kΩ

Operation	Description
Low	SET MODE
High	SA_MODE

- TCON_RDY : Direct 100Ω

Operation	Description
Low → High	Normal Operation
High → Low	Error Operation

- SCL : Pull up 220Ω/3.3kΩ

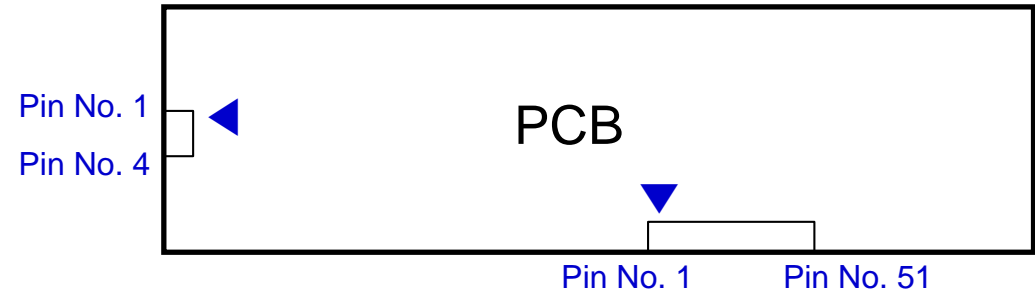
- SDA : Pull up 220Ω/3.3kΩ

- VSYNC : Direct 100Ω

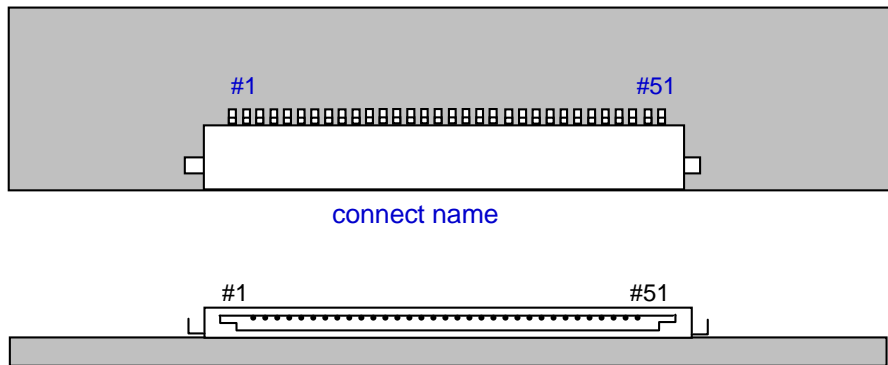
- DCC Table

LUT No.	Description
DCC1	for 120Hz
DCC2	for 120Hz @ Low temp.
DCC3, 4	Not use
DCC5	for 100Hz
DCC6	for 100Hz @ Low temp.
DCC7	Not use
DCC8	DCC off

Note(1) Pin number starts from Right side



A. 51 Pin



B. 4 Pin

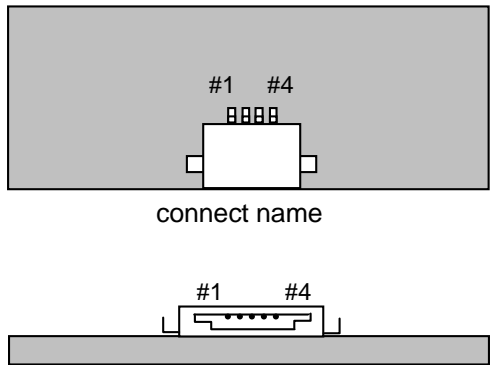


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

5.2 Input Pin Configuration

■ 4pin : DC Voltage

Connector : 51103-0400 (Molex)

Pin No.	Pin Configuration (Function)
1	1 Channel DC Voltage
2	2 Channel DC Voltage
3	No Connection
4	No Connection

■ 5pin : Feedback Voltage

Connector : 51103-0500 (Molex)

Pin No.	Pin Configuration (Function)
1	1 Channel Feedback
2	2 Channel Feedback
3	No Connection
4	No Connection
5	No Connection

5.3 LVDS Interface

	LVDS pin	Odd Data	Even Data
TxOUT/RxIN0	TxIN/RxOUT0	R4	R4
	TxIN/RxOUT1	R5	R5
	TxIN/RxOUT2	R6	R6
	TxIN/RxOUT3	R7	R7
	TxIN/RxOUT4	R8	R8
	TxIN/RxOUT6	R9	R9
	TxIN/RxOUT7	G4	G4
TxOUT/RxIN1	TxIN/RxOUT8	G5	G5
	TxIN/RxOUT9	G6	G6
	TxIN/RxOUT12	G7	G7
	TxIN/RxOUT13	G8	G8
	TxIN/RxOUT14	G9	G9
	TxIN/RxOUT15	B4	B4
	TxIN/RxOUT18	B5	B5
TxOUT/RxIN2	TxIN/RxOUT19	B6	B6
	TxIN/RxOUT20	B7	B7
	TxIN/RxOUT21	B8	B8
	TxIN/RxOUT22	B9	B9
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R2
	TxIN/RxOUT5	R3	R3
	TxIN/RxOUT10	G2	G2
	TxIN/RxOUT11	G3	G3
	TxIN/RxOUT16	B2	B2
	TxIN/RxOUT17	B3	B3
	TxIN/RxOUT23	RESERVED	RESERVED
TxIn/RxIn4	TxIn/RxOUT28	R0	R0
	TxIn/RxOUT29	R1	R1
	TxIn/RxOUT30	G0	G0
	TxIn/RxOUT31	G1	G1
	TxIn/RxOUT32	B0	B0
	TxIn/RxOUT33	B1	B1
	TxIn/RxOUT34	RESERVED	RESERVED

5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (10bit)	DATA SIGNAL																												GRAY SCALE LEVEL								
		RED												GREEN												BLUE												
		R 0	R 1	R 2	R 3	R 4	R 5	R 6	R 7	R 8	R 9	G 0	G 1	G 2	G 3	G 4	G 5	G 6	G 7	G 8	G 9	B 0	B 1	B 2	B 3	B 4	B 5	B 6	B 7		B 8	B 9						
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-							
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-							
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-							
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-							
	RED	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-							
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-						
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-						
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-						
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0							
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1							
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2						
		:	:	:	:	:	:					:	:		:	:	:	:				:	:		:	:		:				R3~ R1020						
		:	:	:	:	:	:					:	:		:	:	:	:				:	:		:	:		:										
	↓ LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021						
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022						
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023						
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0							
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1						
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2						
		:	:	:	:	:	:					:	:	:	:	:	:	:				:	:		:	:	:	:	:	:		G3~ G1020						
		:	:	:	:	:	:					:	:	:	:	:	:	:				:	:		:	:	:	:	:									
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021						
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022						
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023						
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0							
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1						
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2						
		:	:	:	:	:	:					:	:	:	:	:	:	:				:	:	:	:	:	:	:	:	:		B3~ B1020						
		:	:	:	:	:	:					:	:	:	:	:	:	:				:	:	:	:	:	:	:	:									
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021						
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022						
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023						

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

6. EDID Information

6.1 EEPROM Data

EEPROM Data					
No	Item	Spec	Address	Data	Remark
1	Panel Product Vender	XXXX	00	XX	SONY Choice - Note 1
2	Screen Size	XX	01	XX	16進
3	H-Resolution	XXXX	02,03	XX,XX	16進
4	V-Resolution	XXX	04,05	XX,XX	16進
5	Vertical Frequency	XX	06	XX	- Note 2
6	Data Format	XX	07	XX	- Note 3
7	FRC Revision Information	X	ED	XX	ASCII - Note 4
8	Part Number	XXX(X)XXXXXX XX	E0~EC	XX,XX,XX,XX,XX, XX, XX,XX,XX,XX,XX, XX ,XX,XX,XX,XX	ASCII - Note 5, 6

※ Note 1.

Data	Panel Vender Code
00	HDLCD(SEC)
01	Others
02	Others
03	Others
04	Others
05	Others

※ Note 2.

Data	V-Frequency
00	50/60Hz
01	100/120HZ
02	200/240Hz

※ Note 3.

Data	Data Format
00	6Bit
01	8Bit
02	10Bit

※ Note 4.

- FRC Revision Code will be Changed from “00” to “41” (A)
When FRC IC is changed. At First MP. This Code will be “00”

※ Note 5.

- LTY[Z]XXXXXXXX Only for SEC Model

※ Note 6.

- Rest of them must be “00” (Null)

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6.2 EEPROM Data Map

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	XX	XX	XX	XX	XX	XX	XX	XX	00	00	00	00	00	00	00	00
1	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
2	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
3	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
4	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
5	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
6	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
7	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
8	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
9	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
E	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	00	00
F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

7. Interface Timing

7.1 Timing Parameters

7.1.1 (Stand Alone mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	148.5	148.5	148.5	MHz	-
Hsync		F_H	67.5	67.5	67.5	KHz	-
Vsync		F_V	60	60	60	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	1080	1080	1080	lines	-
	Vertical Total	T_{VB}	1125	1125	1125	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	1920	1920	1920	clocks	-
	Horizontal Total	T_H	2200	2200	2200	clocks	-

7.1.2 (SET mode for 96Hz)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	147	148.5	150	MHz	-
Hsync		F_H	67.31	67.99	68.67	KHz	-
Vsync		F_V	46.54	48.01	49.50	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	1080	1080	1080	lines	-
	Vertical Total	T_{VB}	1386	1416	1446	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	1920	1920	1920	clocks	-
	Horizontal Total	T_H	2184	2184	2184	clocks	-

Note) (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal $V_{DD} = 3.3V$

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7.1.3 (Set mode for 100Hz)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	147	148.5	150	MHz	-
Hsync		F_H	67.31	67.99	68.67	KHz	-
Vsync		F_V	48.98	49.99	51.02	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	1080	1080	1080	lines	-
	Vertical Total	T_{VB}	1346	1360	1374	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	1920	1920	1920	clocks	-
	Horizontal Total	T_H	2184	2184	2184	clocks	-

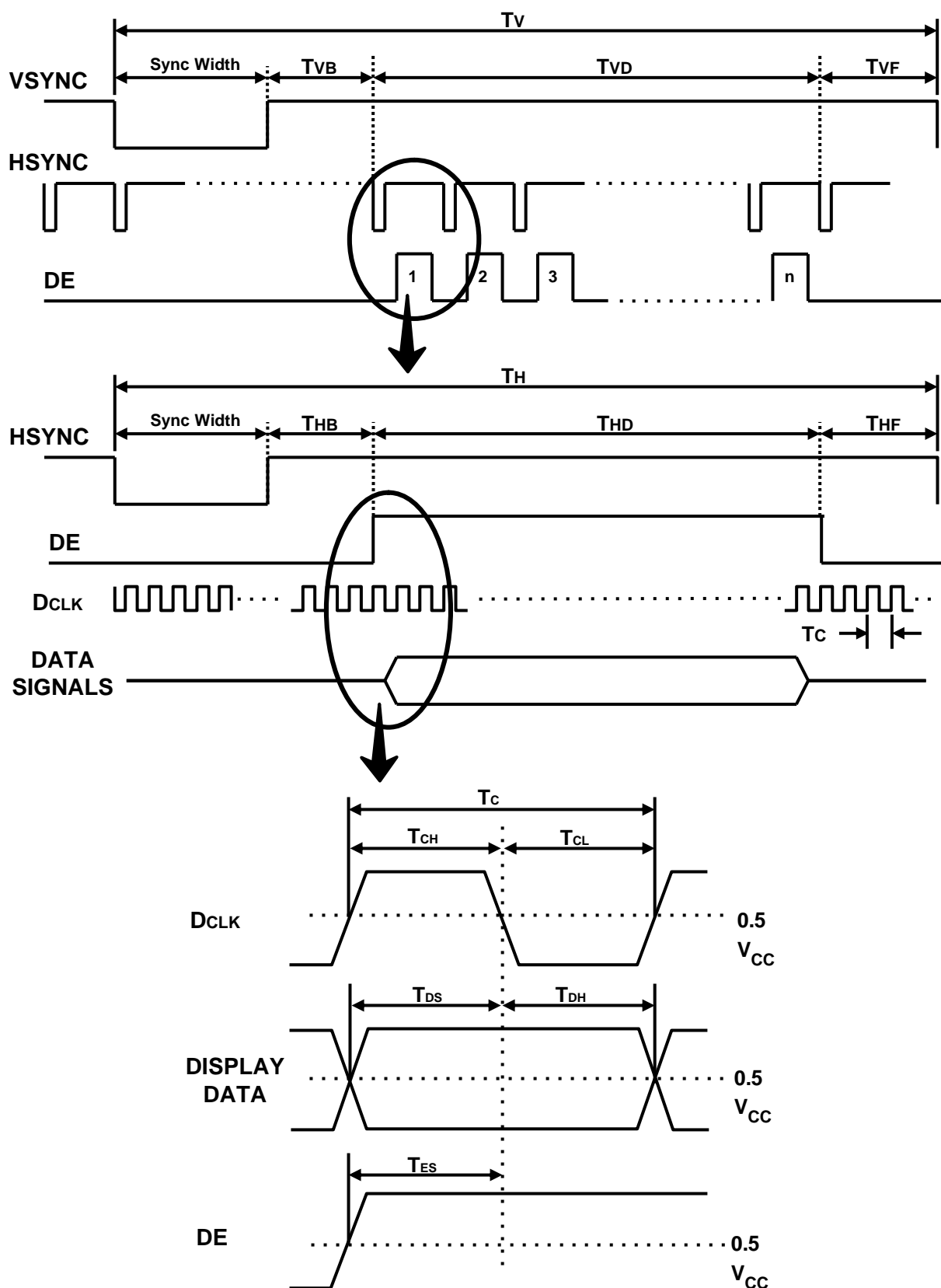
7.1.4 (SET mode for 120Hz)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	147	148.5	150	MHz	-
Hsync		F_H	67.31	67.99	68.67	KHz	-
Vsync		F_V	59.88	59.96	61.08	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	1080	1080	1080	lines	-
	Vertical Total	T_{VB}	1124	1134	1144	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	1920	1920	1920	clocks	-
	Horizontal Total	T_H	2184	2184	2184	clocks	-

Note) (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
(2) Internal $V_{DD} = 3.3V$

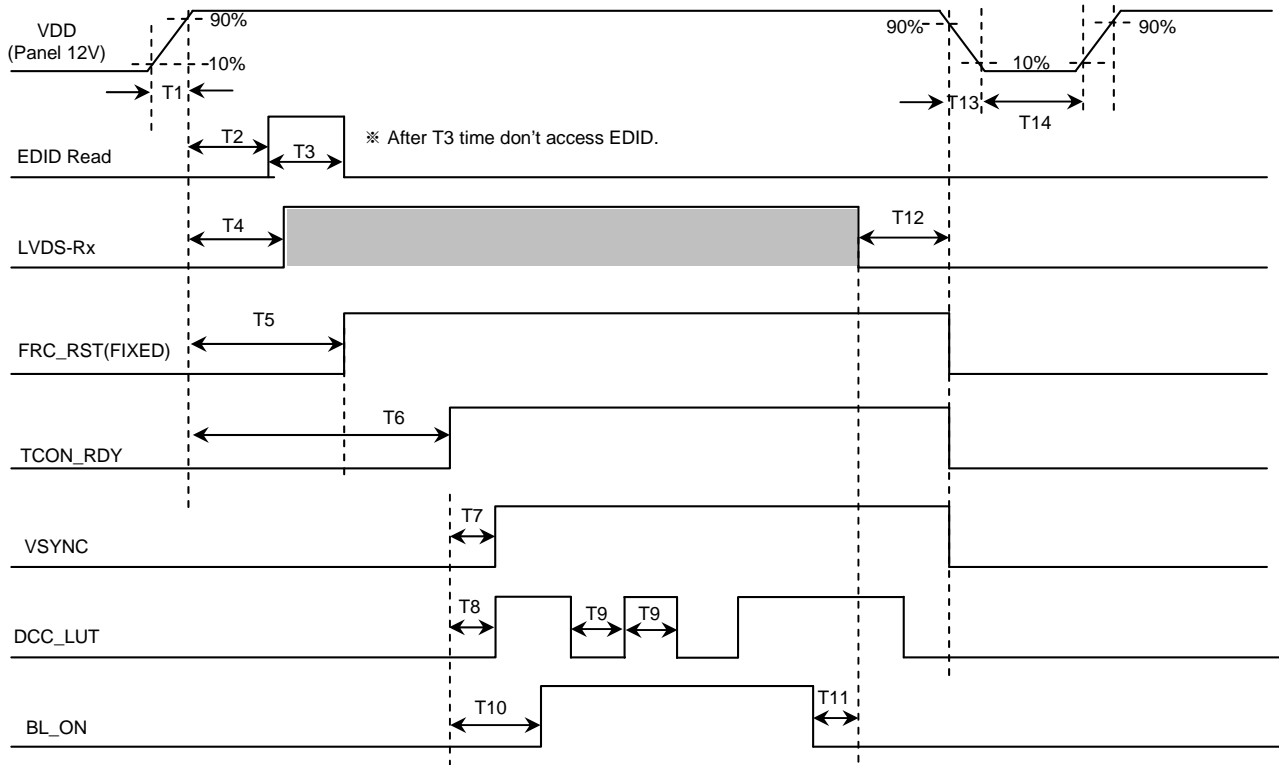
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7.2 Timing diagrams of interface signal



7.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.

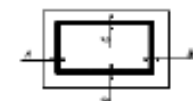
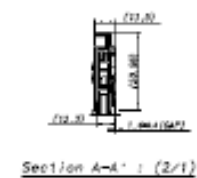
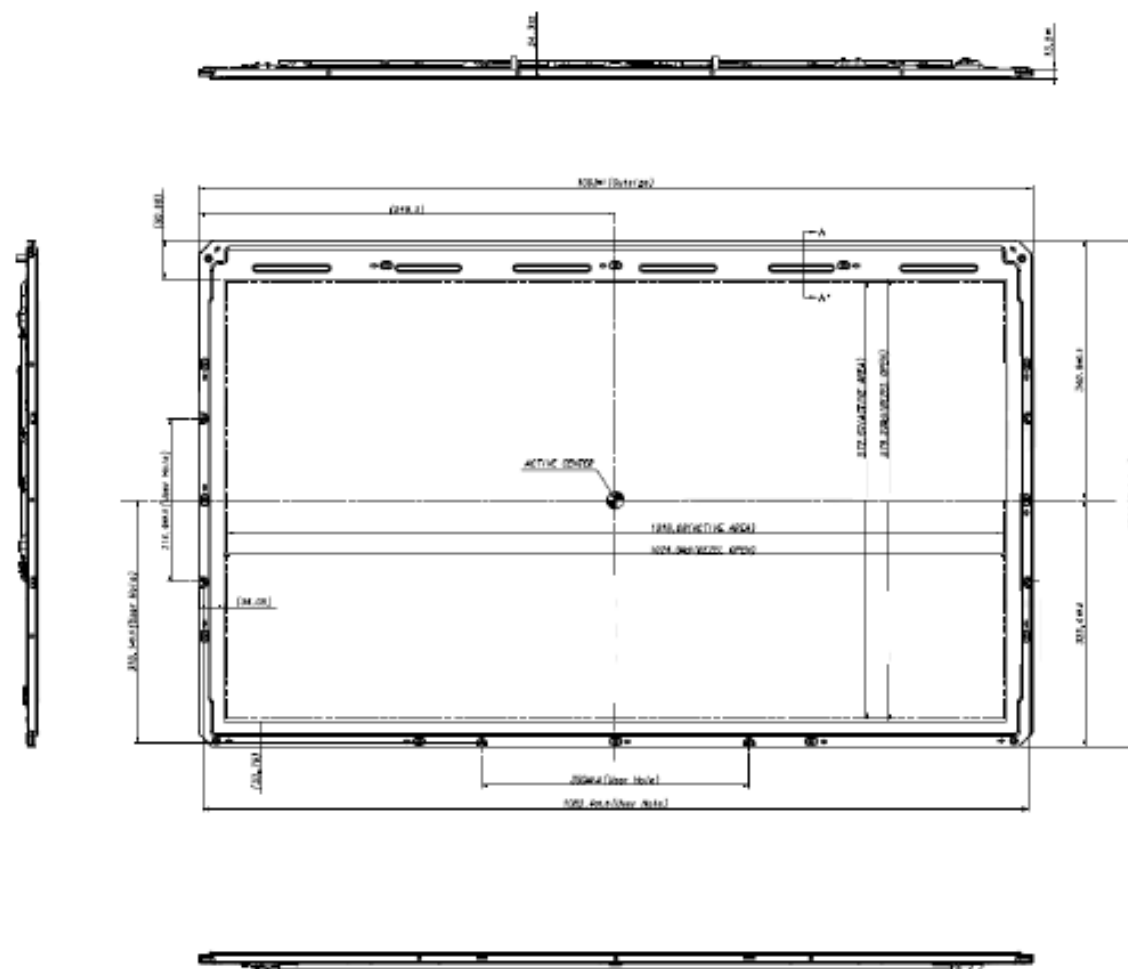


Unit (ms)

	Min	Max		Min	Max		Min	Max
T1	0.47	10	T6	0	450	T11	100	-
T2	0	100	T7	0	-	T12	45	-
T3	0	100	T8	0	-	T13	0	300
T4	200	300	T9	0	-	T14	1000	-
T5	35	75	T10	500	-			

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD} .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T14 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

8. Outline dimension (Front view)



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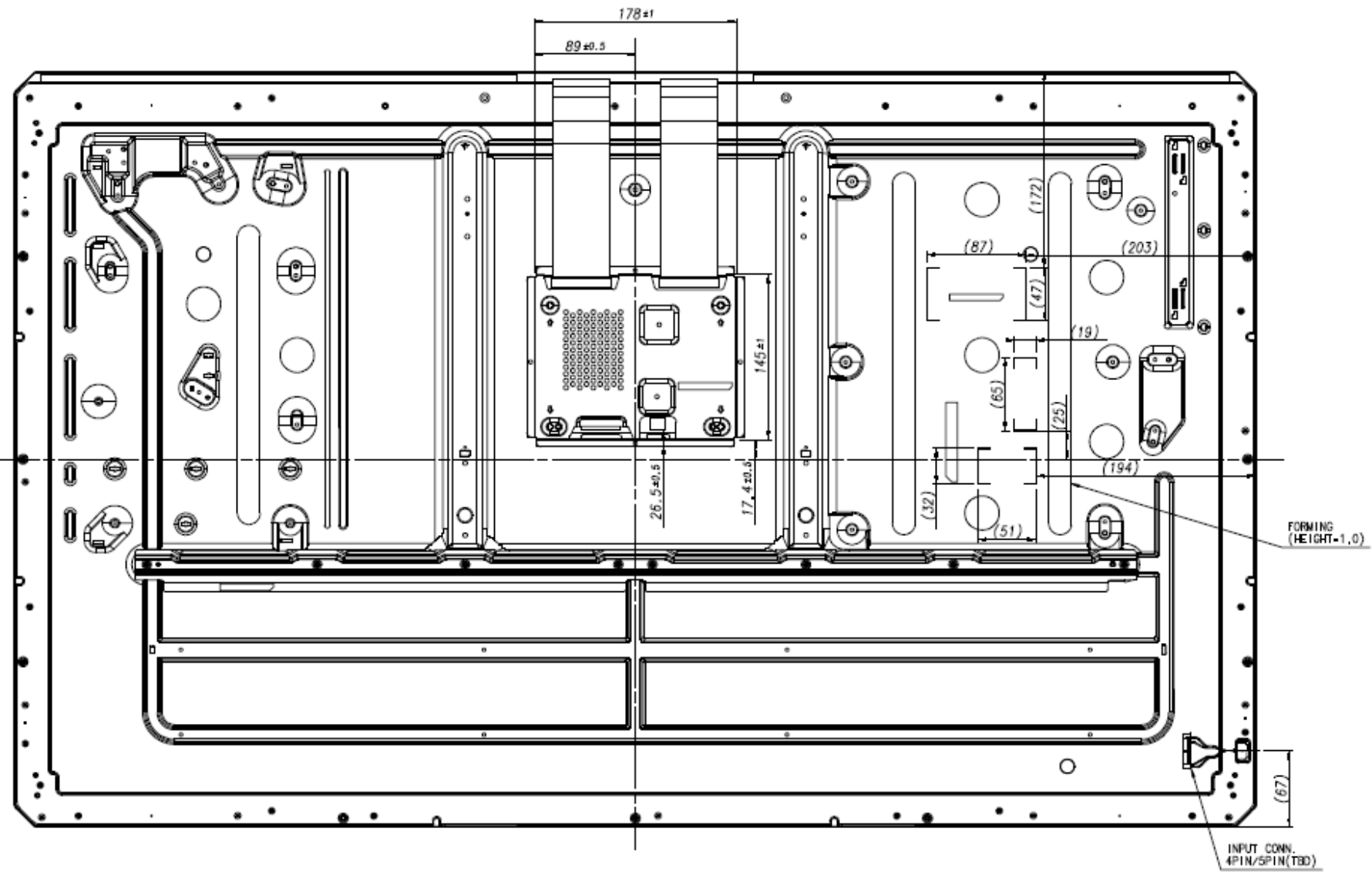
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Outline dimension (Rear View)



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9. EMI Specification

: -3dB at CISPR22 Class B

This EMI Recommendation is recommended to be measured as SET Condition.

10. UL Approval

11. Reliability Test

Item	Test condition	Quantity
Temperature Step Stress	0 ~ 50℃, 10Cycle determination	4EA
HTOL	50℃, 1000hr (500hr determination)	8EA
LTOL	0℃, 1000hr (500hr determination)	4EA
RTOL	20℃, continue ~	4EA
HTS	70℃, 1000hr (500hr determination)	4EA
LTS	-30℃, 1000hr (500hr determination)	4EA
THB	40℃ / 95%RH, 1000hr (500hr determination)	4EA
WHTS	60℃ / 75%RH, 1000hr (500hr determination)	4EA
T/C	-20℃ ~ 60℃, 200cycle (100cycle determination)	4EA
ESD (non-operation)	± 10 kV, 200pF/100Ω, 9Point, 3times/Point	3EA
ESD(operation)	contact : ± 8 kV, 150pF/330Ω, 210Point, 1 time/Point non-contact : ± 15 kV, 150pF/330Ω, 100Point, 1 time/Point	3EA
Input Con. ESD	contact : ± 2kV, 200pF/100, Input Con.Pin, 3 times/Pin	3EA
POWER ON/OFF	30sec (on) / 30sec(off) : 12,000 times	4EA
Vibration	10~300Hz/1.5G/10minSR, XYZ, 30min/axis	3EA
Shock	Condition : 11msec, ± XY/axis 40G & ± Z 1time/axis 30G	3EA
PALLET Vibration	1.05 Grms, 2~200Hz, Random, Z axis 1Hr	1PALLET(17EA)
PALLET Drop	20cm, Bottom, Front, Rear 1times	1PALLET(17EA)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

HTOL/ LTOL : High/Low Temperature Operating Life,
 THB : Temperature Humidity Bias
 HTS/LTS : High/Low Temperature Storage
 WHTS : Wet High Temperature Storage

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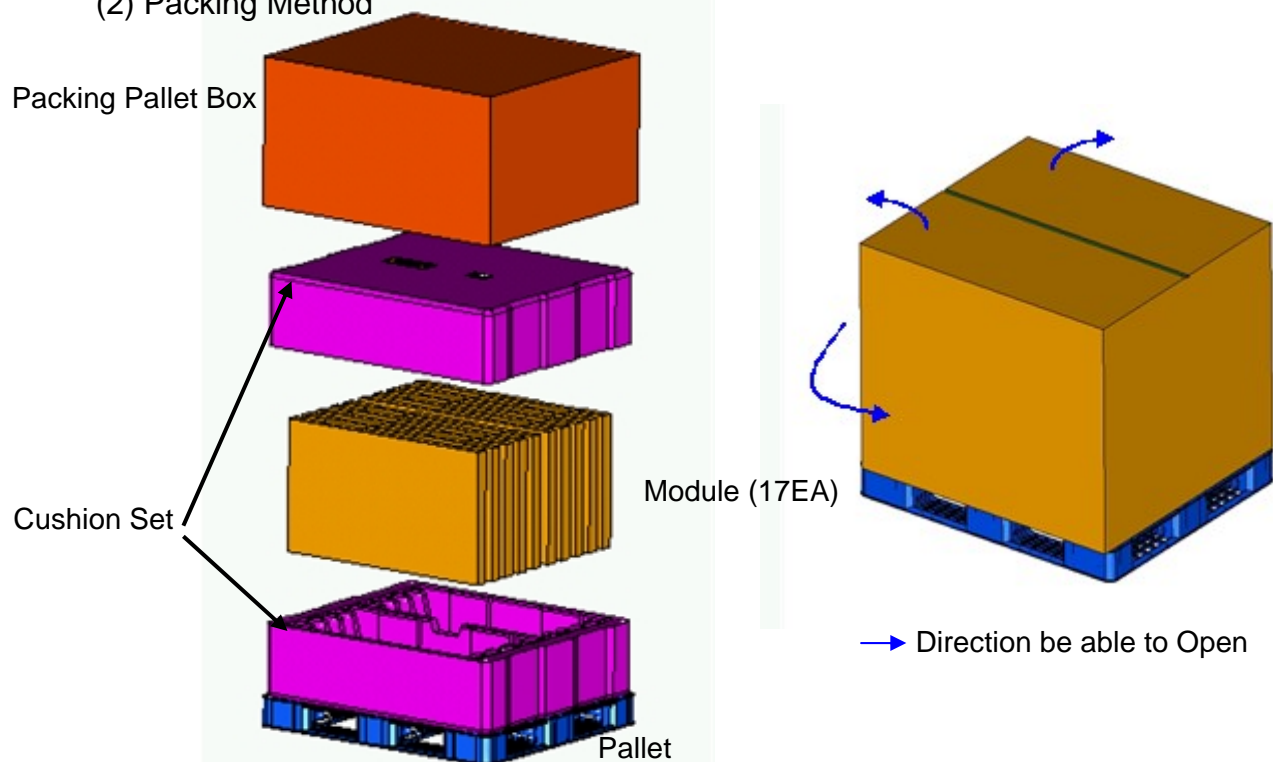
12. PACKING

12.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



8.2 Packing Specification

Item	Specification	Remark
LCD Packing	17 ea / (Packing-Pallet Box)	1. 192.1Kg/LCD(17ea) 2. 7kg/Cushion Set(2ea) 3. 6.7kg/Packing-Pallet Box(1ea) 4. Cushion Material : EPS 5. Packing Pallet Box Material : DW4
Desiccant (Drier)	3ea/LCD	10g/ea, Cobalt-dichloride-free
Pallet	1 Box / Pallet	Pallet weight : 8.8kg
Packing Direction	Vertical	-
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 849mm(Height)
Total Pallet Weight	215.11kg	Pallet(8.8kg) + Module(11.3 x 17 = 192.1kg) + Cushion(7kg) + Pallet-BOX(6.7kg) + Desiccant(0.03kg x 17 = 0.51kg)

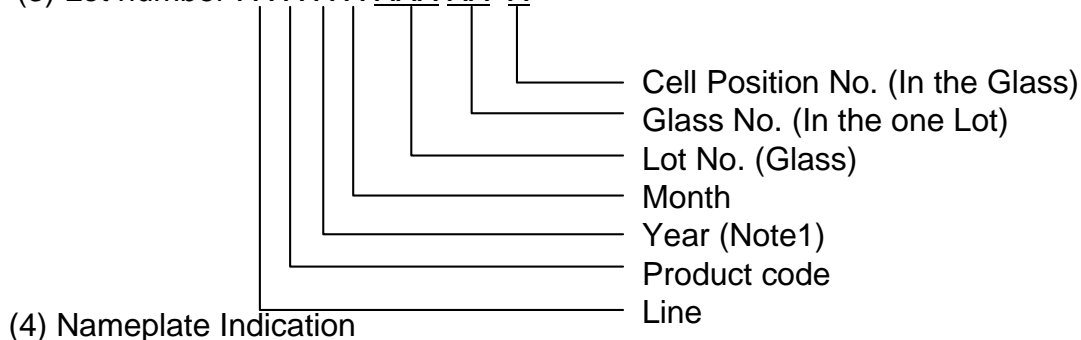
13. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

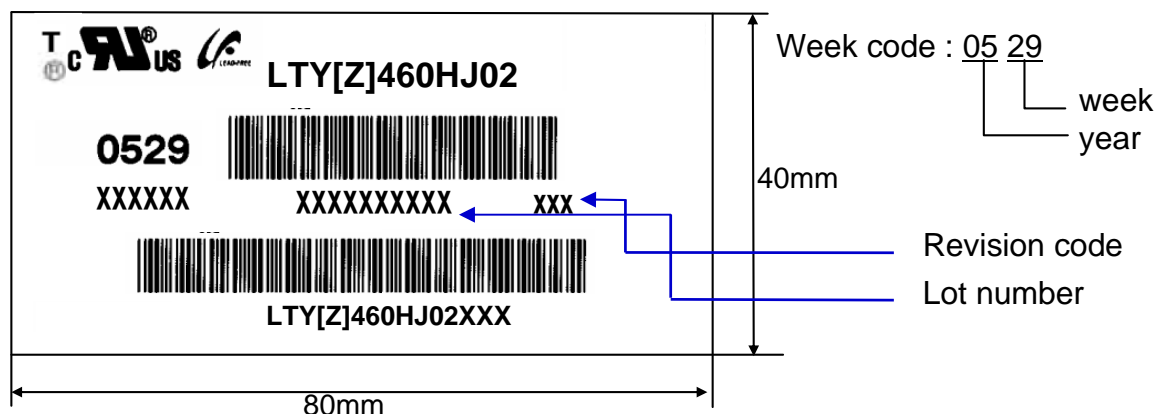
(1) Parts number : LTY[Z]460HJ02-XXX

(2) Revision: One letters

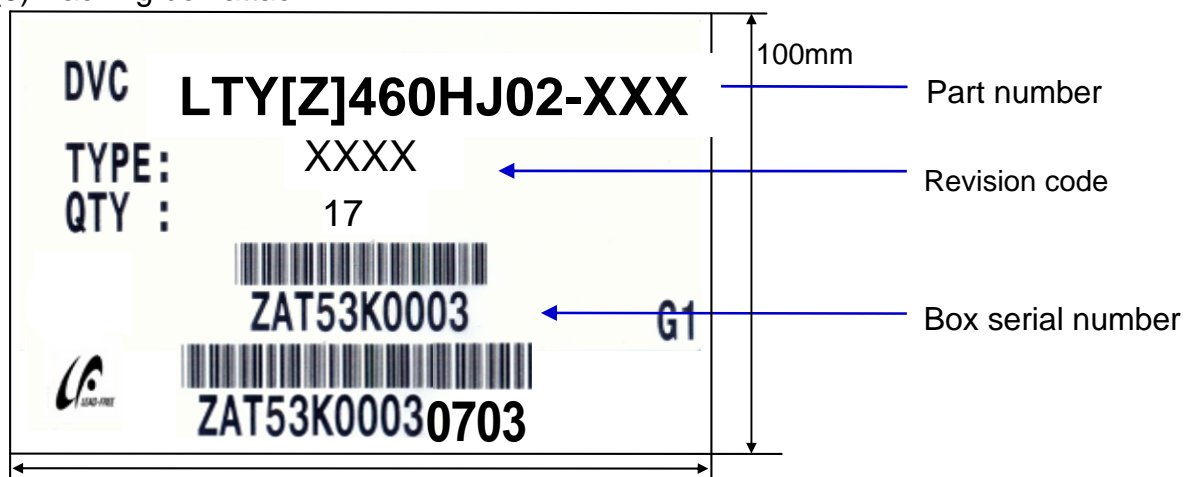
(3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

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14. General Precautions

14.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.
In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily.
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.

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14.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35℃ and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

14.3 Operation

- (a) No Connection or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

14.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
 - Temperature : $20 \pm 15^{\circ}\text{C}$
 - Humidity : $55 \pm 20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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14.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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